Abstract of the Disclosure

Disclosed is a liquid crystal display in which a kick-back voltage of pixels used in the liquid crystal display. The liquid crystal display comprises thin film transistors connected to intersections between a plurality of data lines and a plurality of gate lines; pixel electrodes, each of which is connected to a source of each of the thin film transistors; common electrodes opposed to the pixel electrodes; liquid crystal injected between the pixel electrodes and the common electrodes; a plurality of auxiliary gate lines corresponding to the gate lines; and first capacitors, each of which is connected between the source and each of the auxiliary gate lines. By this construction, even if the voltage of a gate line goes down rapidly, it is possible to minimize variation width of a pixel voltage in the liquid crystal display. Therefore, as compared to the prior art, the liquid crystal display has advantages in that a dynamic range of data line voltage is lower, the adjustment of the common voltage Vcom is unnecessary, a display problem of 30Hz flicker caused by the kick-back voltage ΔVp can be solved, and so forth.